

ICAT at ISIS

Site Report

Jamie



Science & Technology Facilities Council

ISIS

Some Background

- 15.5 million datafiles
 - + 1.25 million in the last year
- 161,000 investigations
 - + 11,000 in the last year



Our Production ICAT Stack

- ICAT 4.7.0
- Topcat 2.3.6
- IDS 1.6.0 – Windows
- All running on Payara 4.1.2 as of 2 weeks ago.





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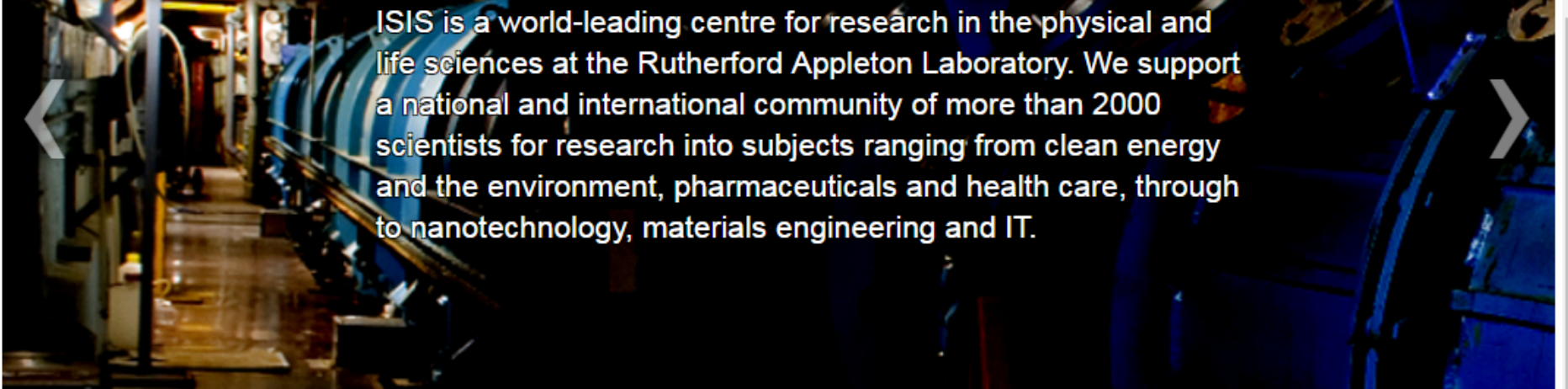
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*Title

*Description

We will evaluate the reliability of modern parallel devices deployed in self-driving carsto dynamically identify/classify objects. Major hardware vendors have focused their research teams on delivering the fastest hardware/software Neural Network (NN) solution, providing a significant edge over competitors. NN algorithms man

*Creators



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Title

Reliability Evaluation of Parallel Processor for Automotive and HPC Applications

Description

We will evaluate the reliability of modern parallel devices deployed in self-driving cars to dynamically identify/classify objects. Major hardware vendors have focused their research teams on delivering the fastest hardware/software Neural Network (NN) solution, providing a significant edge over competitors. NN algorithms map efficiently to parallel devices, exploiting their ability to support data and thread-level parallelism. Parallel devices can detect obstacles in a scene in near real-time, a critical task in next-generation self-driving cars. Researchers have been overly focused on the performance, while neglecting other critical aspects, particularly reliability, in their quest to deliver the most performant configuration. We then need to precisely evaluate the error rate of these systems, and ChiplR is the most suitable beam line for our scope.

Creators

- Dr Paolo Rech
- Dr Carlo Cazzaniga
- Ms Charu Kalra
- Mr Ogun Kibar

Release Date

2017-11-17

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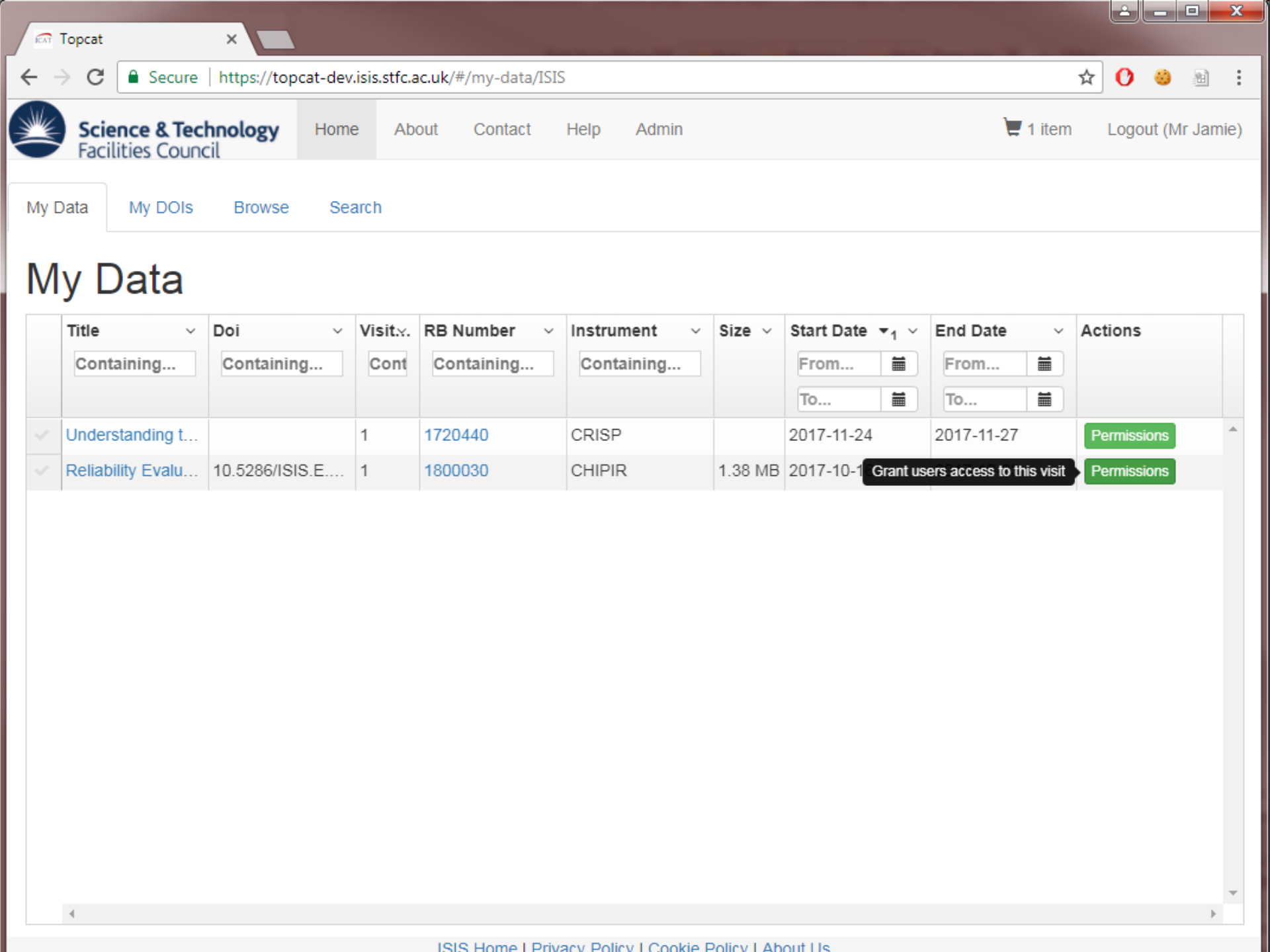
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Dr Jamie

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Proposal: 1800030

Title: Reliability Evaluation of Parallel Processor for Automotive and HPC Applications

Summary: We will evaluate the reliability of modern parallel devices deployed in self-driving cars to dynamically identify/classify objects. Major hardware vendors have focused their research teams on delivering the fastest hardware/software Neural Network (NN) solution, providing a significant edge over competitors. NN algorithms map efficiently to parallel devices, exploiting their ability to support data and thread-level parallelism. Parallel devices can detect obstacles in a scene in near real-time, a critical task in next-generation self-driving cars. Researchers have been overly focused on the performance, while neglecting other critical aspects, particularly reliability, in their quest to deliver the most performant configuration. We then need to precisely evaluate the error rate of these systems, and ChIP is the most suitable beam line for our scene.



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Investigator: Mr Ogun Kibar**Investigator:** Ms Charu Kalra**Investigator:** Dr Carlo Cazzaniga**Investigator:** Dr Jamie**Investigator:** Mr Jamie

Any Questions?



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